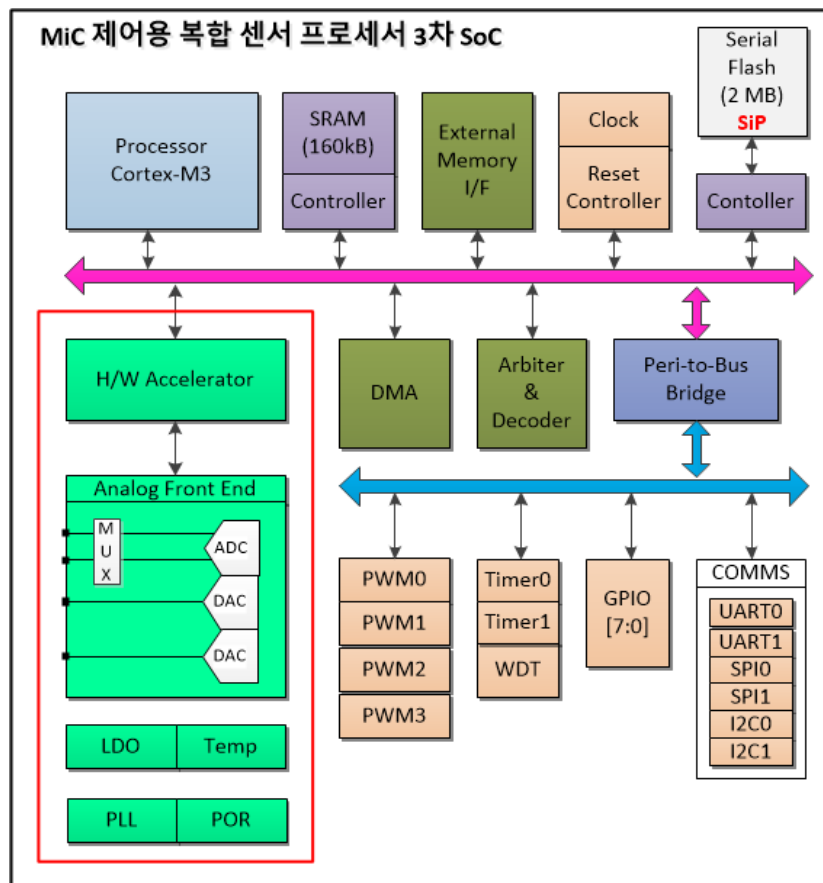


Micro Inverter Controller for Solar cell

Features

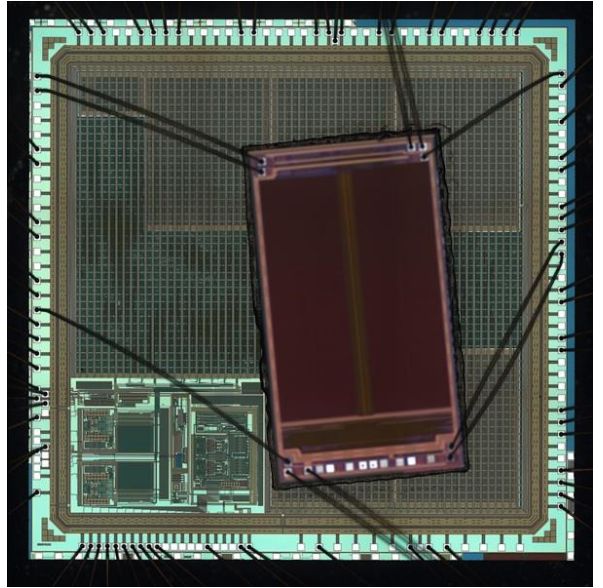
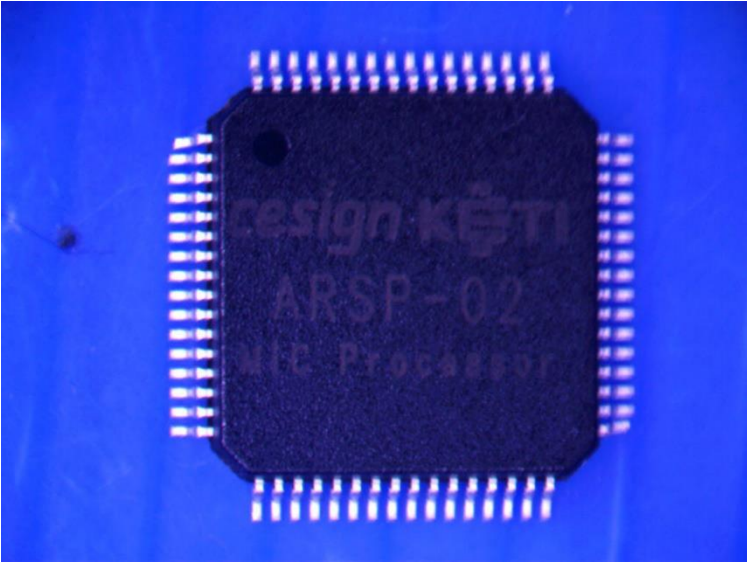
- Processor Core : Cortex-M3
- Memory I/F : SRAM Controller, External Memory I/F, Serial Flash I/F
- BUS : DMA, AHB Bus I/F, Arbiter
- Peripherals : Timer x 2, WDT, PWM x 4, GPIO x 8, UART x 2, SPI x 2, I2C x 2
- Accelerator : Filter Block, Computation Block, PLL Block, Harmonics Block
- Analog : 16-Ch 12-bit ADC, 2-Ch 12-bit DAC, LDO, POR, Temp. Sensor, PLL
- Memory : 160 kB SRAM(내장), 2 MB Serial-Flash (SiP)



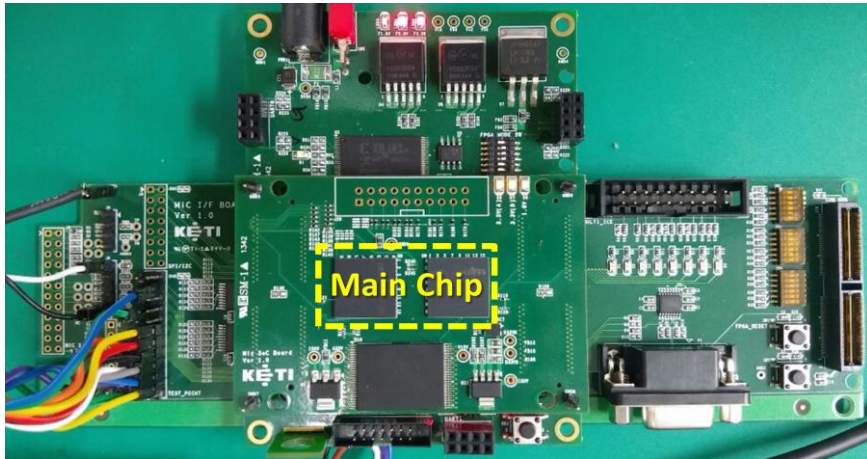
Peripherals

Peripherals	Function			64 PKG	128 PKG
DAC0	VDOUT0			O	O
	VDOUT0B			X	O
DAC1	VDOUT1			O	O
	VDOUT1B			X	O
ADC	AIN[1:7]			O	O
	AIN[8:15]			X	O
Temp. Sensor	ADC-AIN[0]			O	O
SRAM	160 kByte (32kByte SRAM x 5)			O	O
Serial-Flash	2 MByte Serial-Flash (SiP)			O	O
PLL	Clock PLL			O	O
Timer	Timer 0			O	O
	Timer 1			O	O
	Watch-Dog Timer			O	O
PWM	PWM0L	GPO[8]	-	O	O
	PWM0H	GPO[9]	-	O	O
	PWM1L	GPO[10]	-	O	O
	PWM1H	GPO[11]	-	O	O
	PWM2L	GPO[12]	-	O	O
	PWM2H	GPO[13]	-	O	O
	PWM3L	GPO[14]	Analog_PWM0L	O	O
	PWM3H	GPO[15]	Analog_PWM0H	O	O
UART0	UART0_TXD			O	O
	UART0_RXD			O	O
UART1	UART1_TXD			O	O
	UART1_RXD			O	O
I2C0	I2C0_SCL			O	O
	I2C0_SDA			O	O

SPI1	SPI1_MOSI			X	O
	SPI1_MISO				O
	SPI1_SCLK				O
	SPI1_SS				O
GPIO/SPI0/I2C1	GPIO[0]	SPI0_MOSI	-	O	O
	GPIO[1]	SPI0_MISO	-	O	O
	GPIO[2]	SPI0_SCLK	-	O	O
	GPIO[3]	SPI0_SS	-	O	O
	GPIO[4]	I2C1_SDA	-	O	O
	GPIO[5]	I2C1_SCL	-	O	O
	GPIO[6]		-	O	O
	GPIO[7]		-	O	O
EMI	EMI_ADDR[19:0]			X	O
	EMI_DATA[15:0]				
	EMI_CLK				
	EMI_CS0				
	EMI_WRn				
	EMI_RDn				
Serial-Flash (Booting)	SPI_CS			X	O
	SPI_HOLD				
	SPI_DI				
	SPI_DO				
	SPI_CLK				
	SPI_WP				



[Chip Photo]



[EVM Board]